

FIG.1

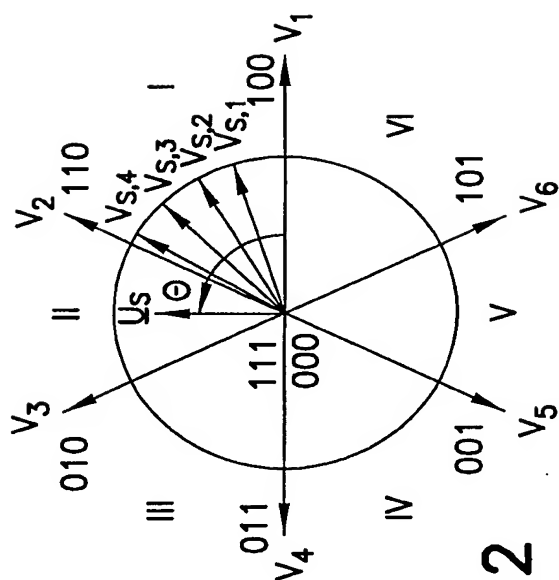
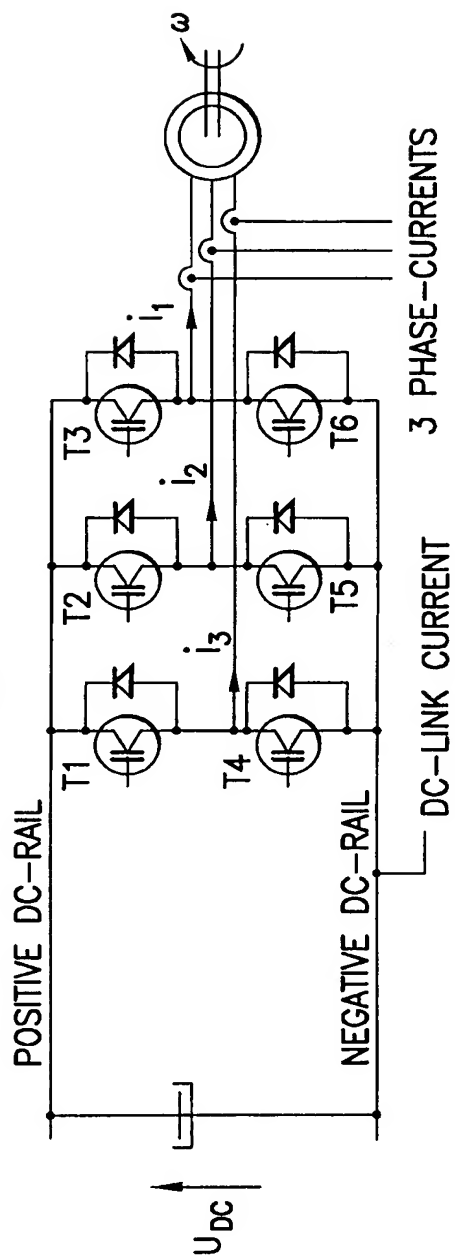


FIG.2

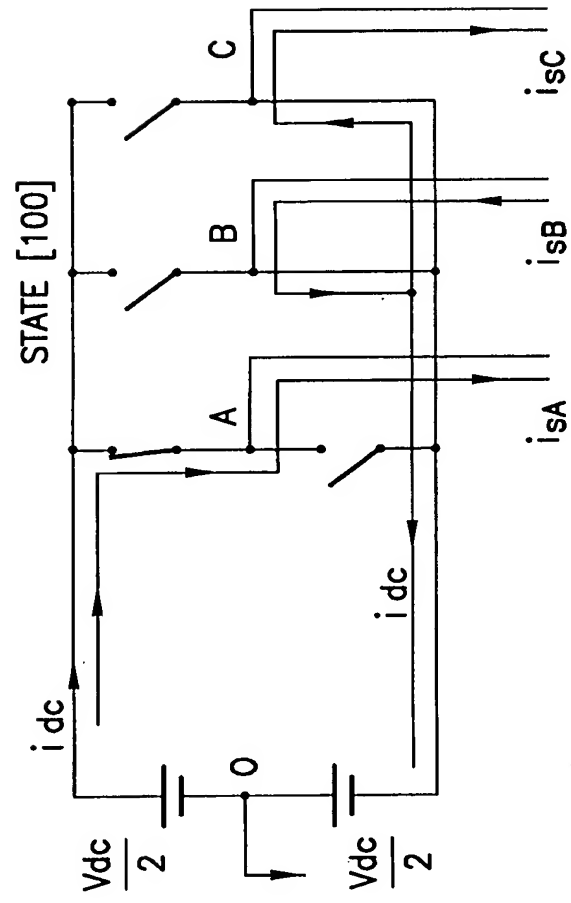


FIG.3

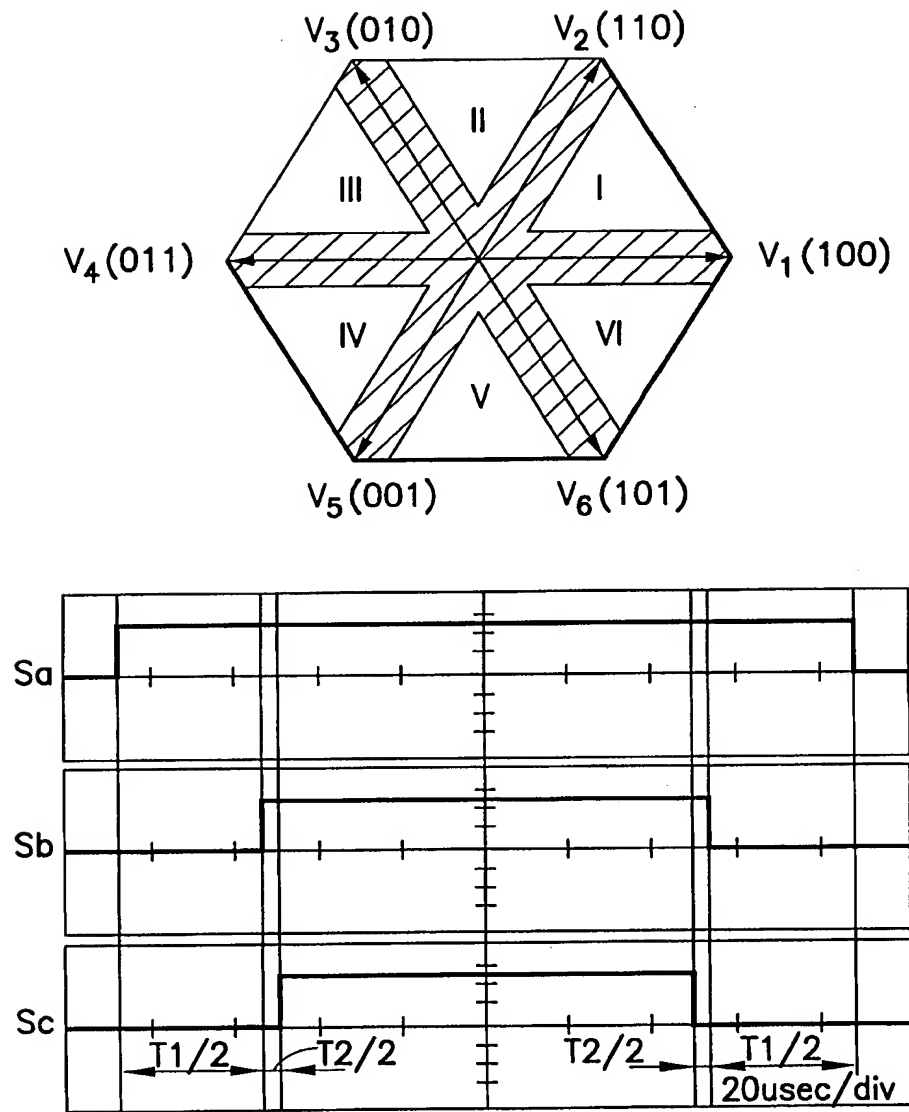


FIG.4

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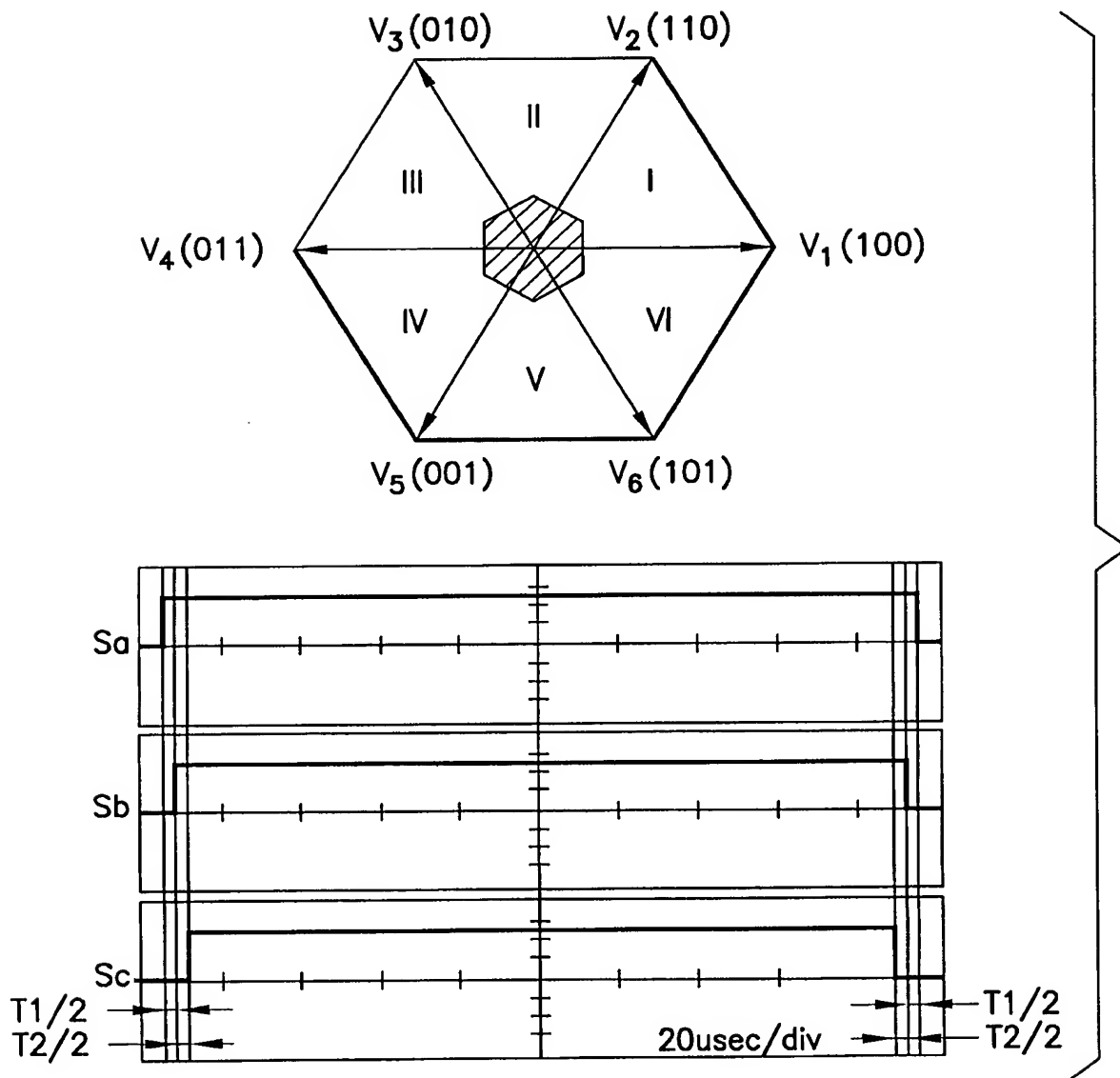
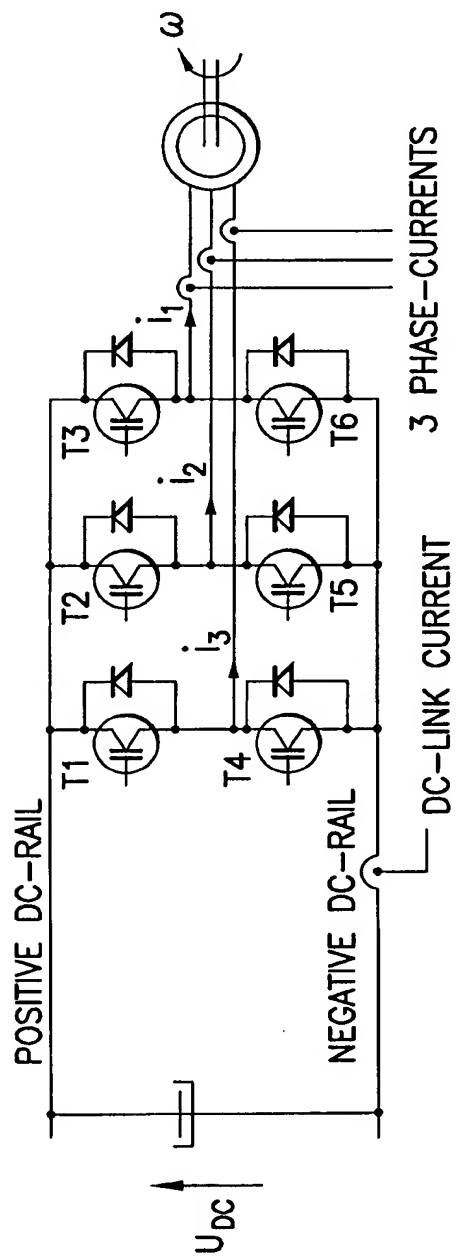
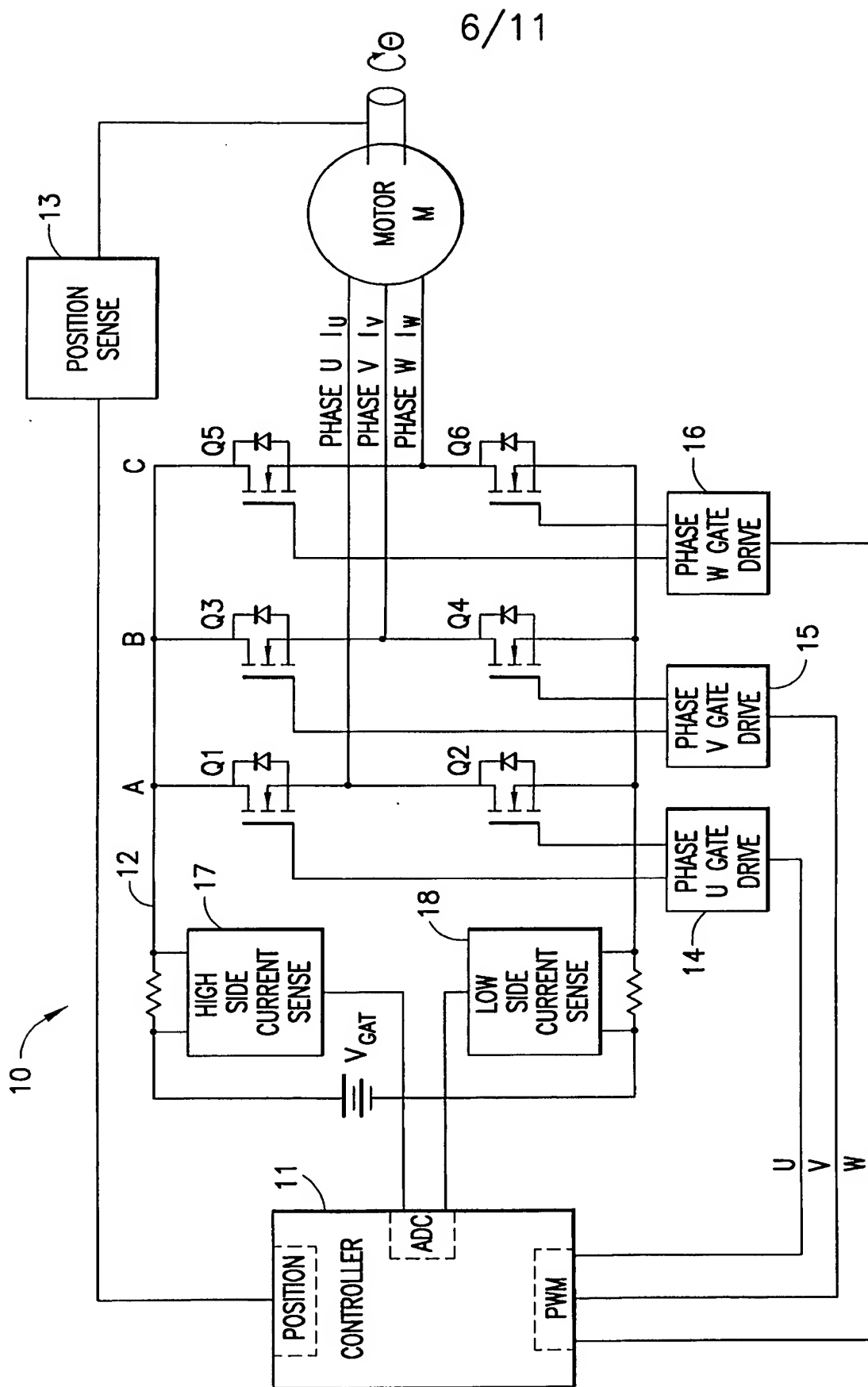


FIG.5



RELATIONSHIP BETWEEN VOLTAGE VECTOR,
DC-LINK CURRENT AND ACTUAL PHASE-CURRENT

VOLTAGE VECTOR	DC-LINK CURRENT i_{DC}
$\underline{U}_S=(100)$	$+ i_1$
$\underline{U}_S=(110)$	$- i_3$
$\underline{U}_S=(010)$	$+ i_2$
$\underline{U}_S=(011)$	$- i_1$
$\underline{U}_S=(001)$	$+ i_3$
$\underline{U}_S=(101)$	$- i_2$
$\underline{U}_S=(000)=(111)$	0



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FIG. 7

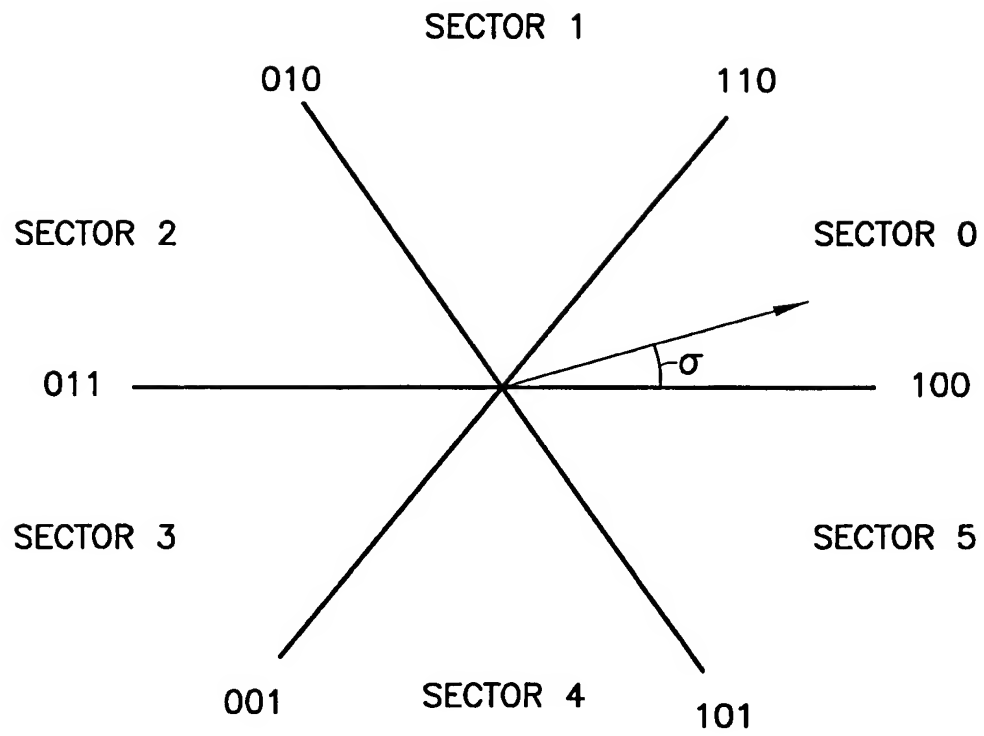


FIG.8

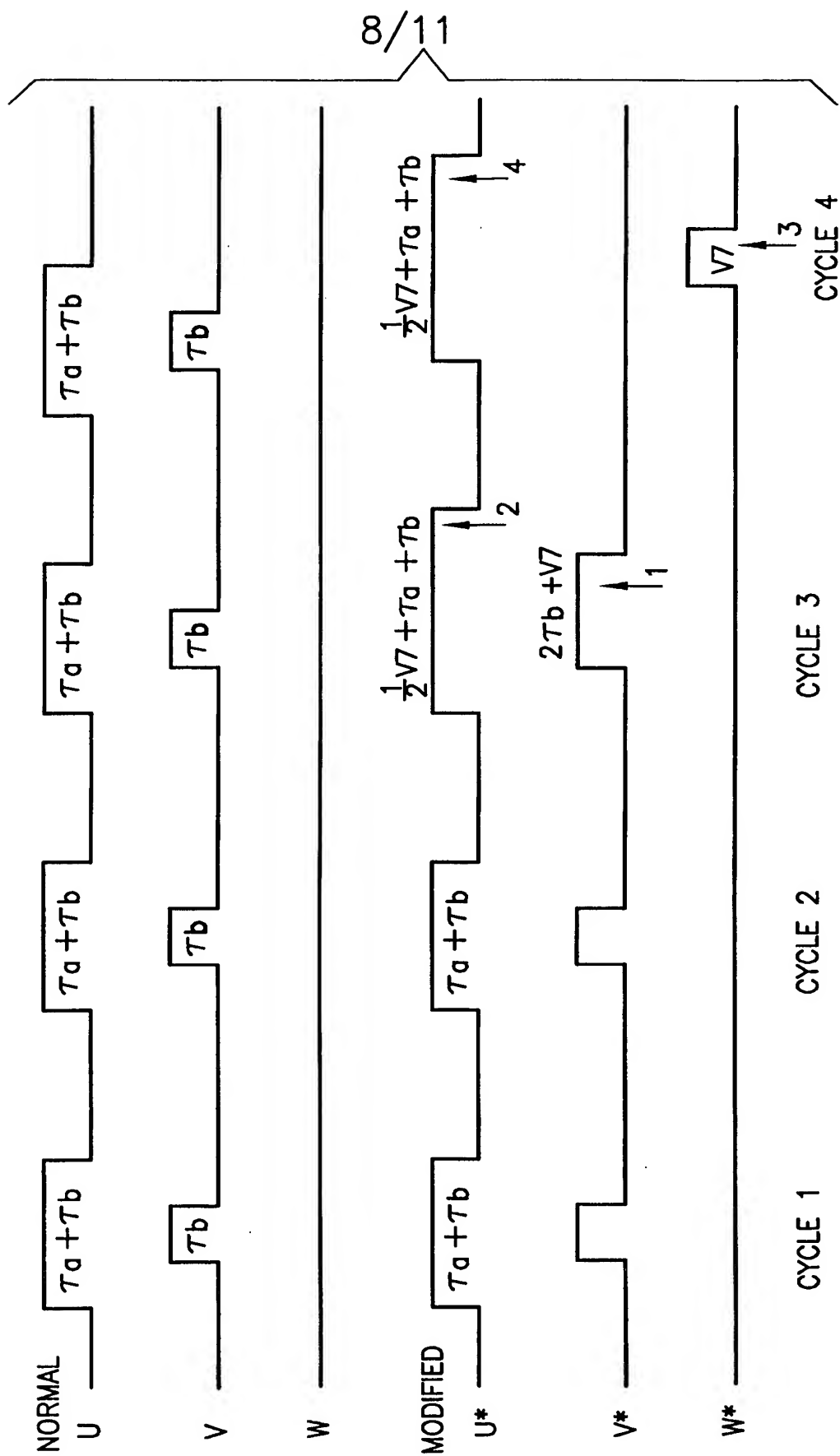


FIG.9

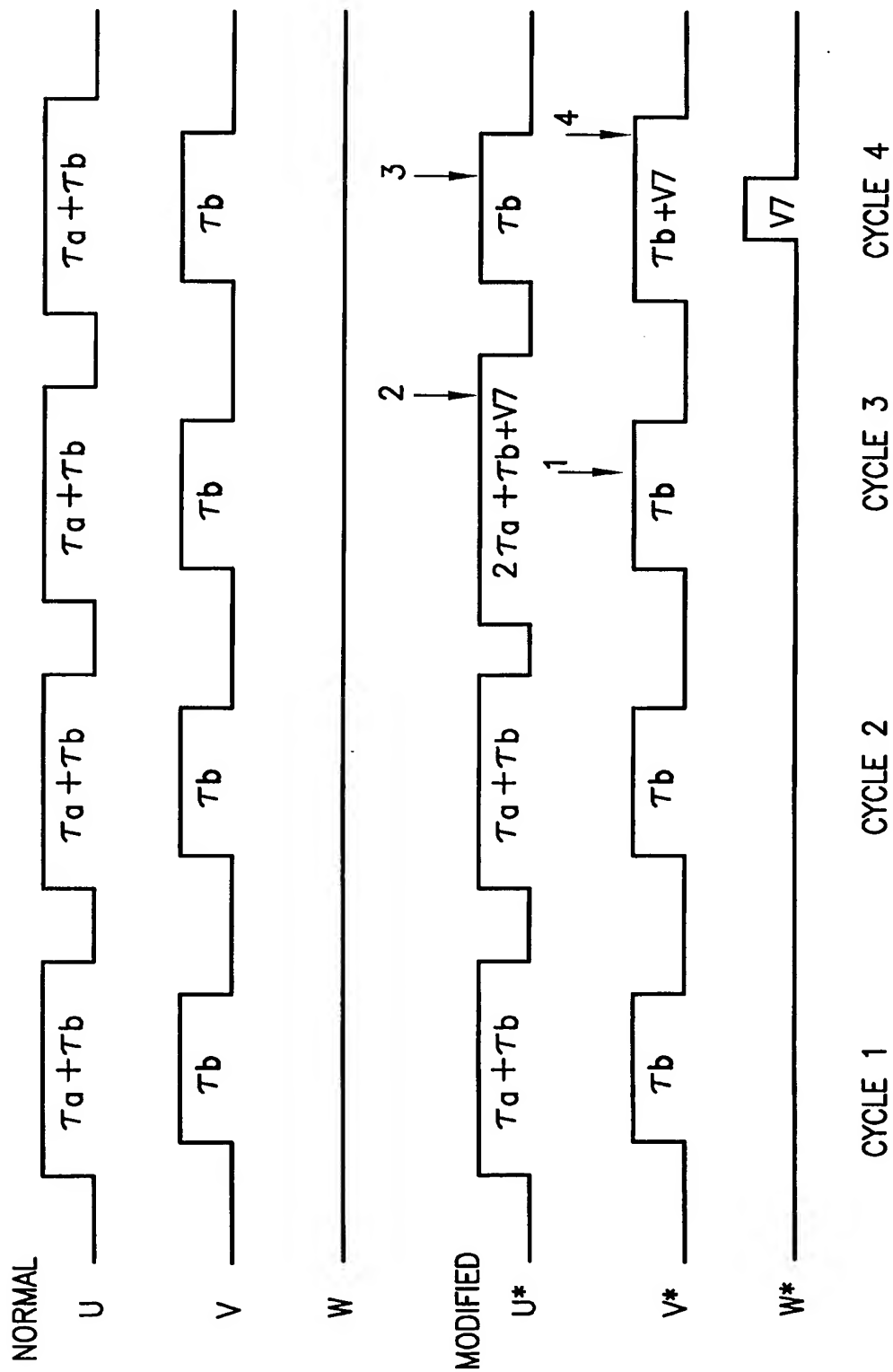


FIG.10

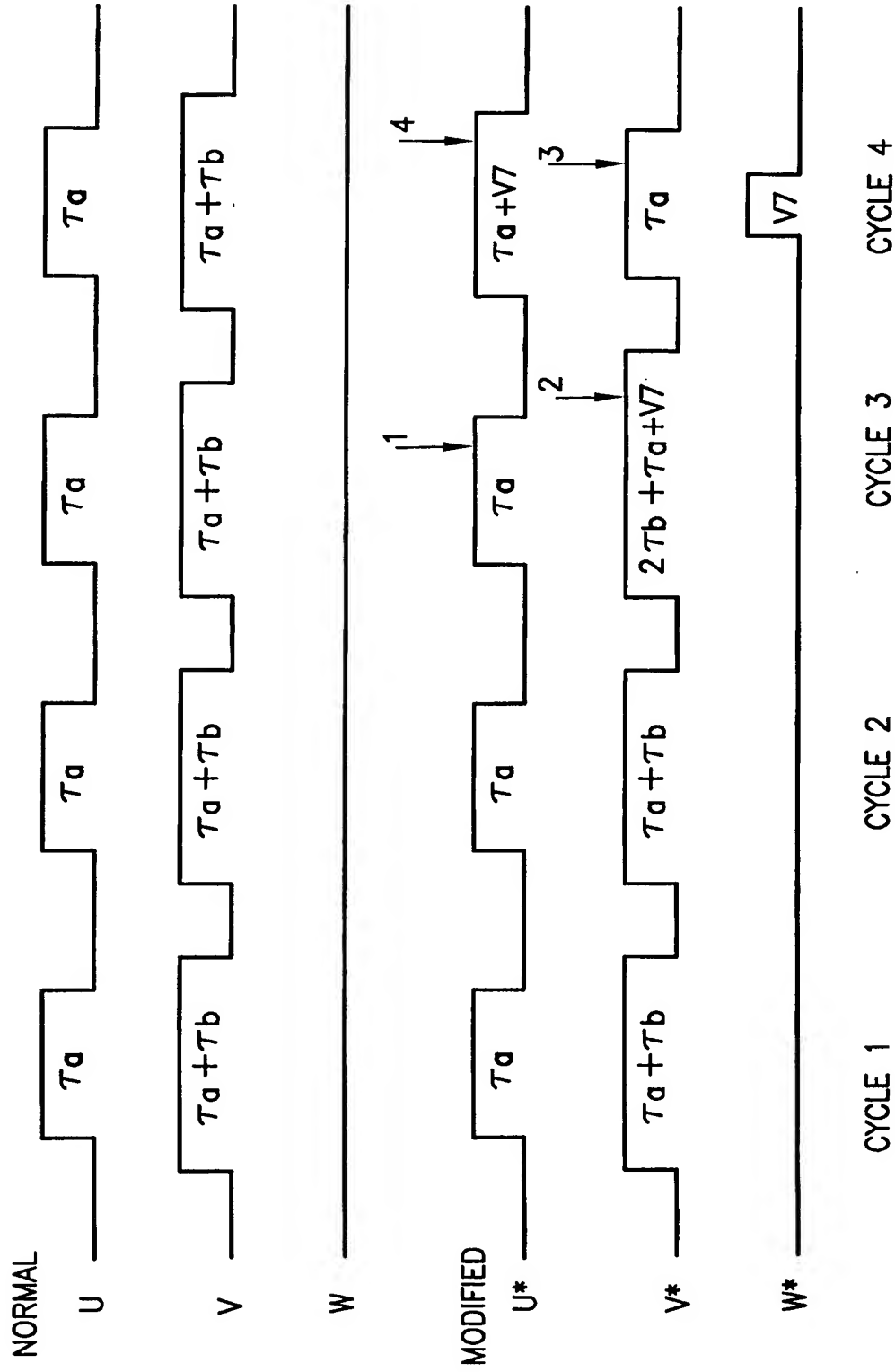


FIG.11

